ATTORNEY'S DOCKET NO. TI-33005 (032350.B372)



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APPLICATION FOR U.S. PATENT UNDER 37 C.F.R. § 1.53(b) TRANSMITTAL FORM

Express Mail Receipt: EL 501024245 US

Box Patent Application ASSISTANT COMMISSIONER FOR PATENTS Washington, D.C. 20231

Inventor or Application Identifier:

Sir:

Transmitted herewith for filing is the original patent application of:

Xiaoju (nmi) Wu

There are the true than the true than	Entitled:	METHOD FOR MANUFACTURING AND STRUCTURE OF SEMICONDUCTOR DEVICE WITH POLYSILICON DEFINITION STRUCTURE						
 -1	Enclosed are:							
X An Assignment of the invention to Texas Instruments Incorporated is attached. (A separate cover sheet in compliance with 37 C.F.R. § 3.28 and § 3.31 is included assignment recordal fee of \$40.00 pursuant to 37 C.F.R. § 1.21(h).								
	X Certificate	e of Mailing (<u>1</u> Total Page)						
	X TI Return Receipt Postcard and Baker Botts Return Receipt Postcard							
	Applicant is: X Large Et Small Et Sm							

_ Small Entity Statement filed in prior application.
Status still proper and desired.

The accompanying application is:	
X Original.	
Continuation Divisional	Continuation-In-Part (CIP)
of prior application No.	which is hereby incorporated by reference therein.

FEE CALCULATION						
	Number		Number Extra	Rate	Basic Fee \$740.00	
Total Claims:	21	-20 =	1	X \$18 =	\$ 18.00	
Independent Claims	2	- 3 =	0	X \$84 =	\$ -0-	
	L FILING FEE =	\$758.00				

X Please charge the filing fee of \$758.00 and any other fee required by this paper, or credit any overpayment, to Deposit Account No. 20-0668 of TEXAS INSTRUMENTS INCORPORATED. This document is being transmitted in duplicate.

Respectfully submitted,

BAKER BOTTS L.L.P. Attorneys for Applicants

Luke K.Pedersen Reg. No. 45,003

CORRESPONDENCE ADDRESS: Jacqueline J. Garner Customer Number 23494 Texas Instruments Incorporated P.O. Box 655474, M/S 3999 Dallas, Texas 75265 Tel. 972.917.5646 Fax. 972.917.4418

Dated: November 29, 2001



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Xiaoju (nmi) Wu

Date Filed:

November 29, 2001

Title:

METHOD FOR MANUFACTURING AND STRUCTURE OF

SEMICONDUCTOR DEVICE WITH POLYSILICON

DEFINITION STRUCTURE

BOX PATENT APPLICATION

Honorable Assistant Commissioner

For Patents

Washington, D.C. 20231

Dear Sir:

CERTIFICATE OF MAILING BY EXPRESS MAIL

I hereby certify that the attached Application Transmittal Form, Utility Patent Application, Utility Fee Transmittal, Declaration and Power of Attorney, Assignment, Assignment Cover Sheet, three (3) sheets of Formal Drawings, and this Certificate of Mailing are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on this 29th day of November, 2001 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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